

INFORMATION DISCLOSURE CITATION
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Applicant	Takashi YOSHIKAWA et al.	
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U.S. PATENT DOCUMENTS

Examiner Initial*		Document Number	Date	Name	Class	Sub Class	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub Class	Translation Yes or No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

T.R.	Sohi, Gurindar S. et al., "Multiscalar Processors", 22 nd International Symposium on Computer Architecture (ISCA-22); (1995).
T.R.	Palacharla, S. et al., " Complexity-Effective Superscalar Processors", 24 th International Symposium on Computer Architecture (ISCA-24), pp. 1-13, (1997).
T.R.	Mahlke, Scott A. et al., " A Comparison of Full and Partial Predicated Execution Support for ILP Processors", (ISCA-22), pp. 1-12, June 1995.

Examiner T.R. Date Considered 10/24/03

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.